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Michalopouios, D.A.;
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2. **Design considerations for a fiber optic communications network for pow**
Kirkham, H.; Johnston, A.R.; Allen, G.D.;
Power Delivery, IEEE Transactions on
Volume 9, Issue 1, Jan. 1994 Page(s):510 - 518
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3. **Testbed-based validation of design techniques for reliable distributed rea**
Chu, W.W.; Kim, K.H.; McDonald, W.C.;
Proceedings of the IEEE
Volume 75, Issue 5, May 1987 Page(s):649 - 667
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Smith, E.;
Communications, IEEE Transactions on [legacy, pre - 1988]
Volume 23, Issue 1, Jan 1975 Page(s):111 - 120
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5. **Queueing Models for Computer Communications System Analysis**
Kobayashi, H.; Konheim, A.;
Communications, IEEE Transactions on [legacy, pre - 1988]
Volume 25, Issue 1, Jan 1977 Page(s):2 - 29
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6. **Air traffic control: a system-wide upgrade races to replace two-decade-ol**

Perry, T.S.;
Spectrum, IEEE
Volume 28, Issue 2, Feb. 1991 Page(s):22 - 27
Digital Object Identifier 10.1109/6.100904
AbstractPlus | Full Text: PDF(817 KB) IEEE JNL
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7. **The rise and fall of the General Electric Corporation computer departmen**
Lee, J.A.N.;
Annals of the History of Computing, IEEE
Volume 17, Issue 4, Winter 1995 Page(s):24 - 45
Digital Object Identifier 10.1109/85.477434
AbstractPlus | References | Full Text: PDF(2812 KB) IEEE JNL
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8. **A virtual studio for live broadcasting: the Mona Lisa project**
Blonde, L.; Buck, M.; Galli, R.; Niem, W.; Paker, Y.; Schmidt, W.; Thomas, G.;
Multimedia, IEEE
Volume 3, Issue 2, Summer 1996 Page(s):18 - 29
Digital Object Identifier 10.1109/93.502291
AbstractPlus | References | Full Text: PDF(2136 KB) IEEE JNL
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9. **An overview of reflective memory systems**
Jovanovic, M.; Milutinovic, V.;
Concurrency, IEEE [see also IEEE Parallel & Distributed Technology]
Volume 7, Issue 2, April-June 1999 Page(s):56 - 64
Digital Object Identifier 10.1109/4434.766965
AbstractPlus | References | Full Text: PDF(128 KB) IEEE JNL
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10. **IBM field engineering experiences: a personal memoir**
Meyers, G.E.;
Annals of the History of Computing, IEEE
Volume 21, Issue 4, Oct.-Dec. 1999 Page(s):72 - 76
Digital Object Identifier 10.1109/85.801537
AbstractPlus | Full Text: PDF(500 KB) IEEE JNL
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11. **Sperry Rand's third-generation computers 1964-1980**
Gray, G.T.; Smith, R.Q.;
Annals of the History of Computing, IEEE
Volume 23, Issue 1, Jan.-March 2001 Page(s):3 - 16
Digital Object Identifier 10.1109/85.910845
AbstractPlus | References | Full Text: PDF(404 KB) IEEE JNL
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12. **Performance analysis of multiprocessor DSPs: a stream-oriented compo**
Thiele, L.; Wandeler, E.; Chakraborty, S.;
Signal Processing Magazine, IEEE
Volume 22, Issue 3, May 2005 Page(s):38 - 46
AbstractPlus | Full Text: PDF(777 KB) IEEE JNL
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13. **Design of a New On-Board Computer for the New KOMPSAT Bus**
Day-Young Kim; Ki-Ho Kwon; Jong-Wook Choi; Jong-In Lee; Hak-Jung Kim;
Aerospace, 2005 IEEE Conference
5-12 March 2005 Page(s):1 - 12
Digital Object Identifier 10.1109/AERO.2005.1559535

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☐ **14. Reconfigurable Computing for RC6 Cryptography**
Itani, M.; Diab, H.;
Pervasive Services, 2004. ICPS 2004. IEEE/ACS International Conference on
19-23 July 2004 Page(s):121 - 127
Digital Object Identifier 10.1109/PERSER.2004.25
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☐ **15. A 40 Gb/s network processor with PISC/spl trade/ dataflow architecture**
Carlstrom, J.; Nordmark, G.; Roos, J.; Boden, T.; Svensson, L.-O.; Westlund, I
Solid-State Circuits Conference, 2004. Digest of Technical Papers. ISSCC. 20
International
15-19 Feb. 2004 Page(s):60 - 61 Vol.1
Digital Object Identifier 10.1109/ISSCC.2004.1332593
AbstractPlus | Full Text: PDF(314 KB) | Multimedia IEEE CNF
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☐ **16. Flexible and efficient parallel I/O for large-scale multi-component simulat**
Xiaosong Ma; Xiangmin Jiao; Campbell, M.; Winslett, M.;
Parallel and Distributed Processing Symposium, 2003. Proceedings. Internatic
22-26 April 2003 Page(s):10 pp.
Digital Object Identifier 10.1109/IPDPS.2003.1213462
AbstractPlus | Full Text: PDF(397 KB) IEEE CNF
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☐ **17. Using I_2O and I/O processors in embedded PCI systems. New software st processor type speed embedded PCI implementations**
Robinson, R.;
WESCON/98
15-17 Sept. 1998 Page(s):204 - 209
Digital Object Identifier 10.1109/WESCON.1998.716449
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☐ **18. A dynamic mechanical load emulation test facility to evaluate the perforn inverters**
Hewson, C.R.; Asher, G.M.; Sumner, M.;
Power Electronics and Variable Speed Drives, 1998. Seventh International Co
Conf. Publ. No. 456)
21-23 Sept. 1998 Page(s):206 - 211
AbstractPlus | Full Text: PDF(512 KB) IEE CNF

☐ **19. IEEE guide for computer-based control for hydroelectric power plant aut**
IEEE Std 1249-1996
6 May 1997
AbstractPlus | Full Text: PDF(688 KB) IEEE STD

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1 **A development platform for wireless Internet connected robotic devices**

Rathika Rajaravivarma, Leo Cetinski

December 2005 **Journal of Computing Sciences in Colleges**, Volume 21 Issue 2

Publisher: Consortium for Computing Sciences in Colleges

Full text available: pdf(434.72 KB) Additional Information: full citation, abstract, references, index terms

This paper addresses the demands of project based educational need at Central Connecticut State University (CCSU) to stay up to date with the technological developments in the ever-changing wireless communication field. The targeted audiences are from the ABET accredited Computer Science program students in their senior year of the undergraduate program and/or the students in the graduate program with concentration in networking or communication. This project provides a platform for a hands-on e ...

2 **Highly available systems for database applications**

Won Kim

March 1984 **ACM Computing Surveys (CSUR)**, Volume 16 Issue 1

Publisher: ACM Press

Full text available: pdf(2.43 MB) Additional Information: full citation, abstract, references, citings, index terms, review

As users entrust more and more of their applications to computer systems, the need for systems that are continuously operational (24 hours per day) has become even greater. This paper presents a survey and analysis of representative architectures and techniques that have been developed for constructing highly available systems for database applications. It then proposes a design of a distributed software subsystem that can serve as a unified framework for constructing database applica ...

3 **Poster session 2: Energy estimation of peripheral devices in embedded systems**

Ozgur Celebican, Tajana Simunic Rosing, Vincent J. Mooney

April 2004 **Proceedings of the 14th ACM Great Lakes symposium on VLSI**

Publisher: ACM Press

Full text available: pdf(188.18 KB) Additional Information: full citation, abstract, references, index terms

This paper introduces a methodology for estimation of energy consumption in peripherals such as audio and video devices. Peripherals can be responsible for significant amount of the energy consumption in current embedded systems. We introduce a cycle-accurate

energy simulator and profiler capable of simulating peripheral devices. Our energy estimation tool for peripherals can be useful for hardware and software energy optimization of multimedia applications and device drivers. The simulator and ...

Keywords: audio, device drivers, energy estimation, software optimization

4 A multiprocessing system for the direct execution of LISP

Rhon Williams
August 1978 **ACM SIGMOD Record , ACM SIGIR Forum , ACM SIGARCH Computer Architecture News**, Volume 10 , 13 , 7 Issue 1 , 2 , 2
Publisher: ACM Press
Full text available: pdf(691.35 KB) Additional Information: full citation, abstract, references, citings

Current implementations were found to be impractical for airborne use due to LISP's incompatability with conventional computer architectures. Direct execution of LISP with tasks distributed between three processors, seemed to be a workable solution. The language was analyzed, and a special token was devised, using a descriptor with a single pointer. Through careful distribution of responsibilities, control and data flow between the processors was minimized. Significant memory savings resulted fr ...

5 Two implementations of the 'FLEX' machine

John Kershaw
December 1981 **ACM SIGMICRO Newsletter , Proceedings of the 14th annual workshop on Microprogramming MICRO 14**, Volume 12 Issue 4
Publisher: IEEE Press, ACM Press
Full text available: pdf(1.36 MB) Additional Information: full citation, abstract, references, index terms

The FLEX high-level language architecture is introduced. Two microprogrammed implementations of FLEX are described, one based on a special purpose, horizontally coded machine and the other on a general purpose emulator called GEMINI. Examples and statistics of both microprograms are given and the two implementations compared.

6 16.4-Tflops direct numerical simulation of turbulence by a Fourier spectral method on the Earth Simulator

Mitsuo Yokokawa, Ken'ichi Itakura, Atsuya Uno, Takashi Ishihara, Yukio Kaneda
November 2002 **Proceedings of the 2002 ACM/IEEE conference on Supercomputing**
Publisher: IEEE Computer Society Press
Full text available: pdf(3.59 MB) Additional Information: full citation, abstract, references, index terms

The high-resolution direct numerical simulations (DNSs) of incompressible turbulence with numbers of grid points up to 4096^3 have been executed on the Earth Simulator (ES). The DNSs are based on the Fourier spectral method, so that the equation for mass conservation is accurately solved. In DNS based on the spectral method, most of the computation time is consumed in calculating the three-dimensional (3D) Fast Fourier Transform (FFT), which requires huge-scale global data transfer and ...

7 A multiprocessing system for the direct execution of LISP

Rhon Williams
August 1978 **Proceedings of the fourth workshop on Computer architecture for non-numeric processing**
Publisher: ACM Press
Full text available: pdf(599.89 KB) Additional Information: full citation, abstract, references, citings, index terms

Current implementations were found to be impractical for airborne use due to LISP's incompatability with conventional computer architectures. Direct execution of LISP with

tasks distributed between three processors, seemed to be a workable solution. The language was analyzed, and a special token was devised, using a descriptor with a single pointer. Through careful distribution of responsibilities, control and data flow between the processors was minimized. Significant memory savings result ...

8 Failure correction techniques for large disk arrays

G. A. Gibson, L. Hellerstein, R. M. Karp, D. A. Patterson

April 1989 **ACM SIGARCH Computer Architecture News , Proceedings of the third international conference on Architectural support for programming languages and operating systems ASPLOS-III**, Volume 17 Issue 2

Publisher: ACM Press

Full text available: pdf(1.24 MB) Additional Information: full citation, abstract, references, citings, index terms

The ever increasing need for I/O bandwidth will be met with ever larger arrays of disks. These arrays require redundancy to protect against data loss. This paper examines alternative choices for encodings, or codes, that reliably store information in disk arrays. Codes are selected to maximize mean time to data loss or minimize disks containing redundant data, but are all constrained to minimize performance penalties associated with updating information or recovering from catastroph ...

Results 1 - 8 of 8

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	12775	(network adj attached adj storage) or NAS	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/23 09:21
S2	460	((network adj attached adj storage) or NAS) same processor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/22 17:36
S3	25	((network adj attached adj storage) or NAS) same processor same chassis	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/22 17:47
S4	135	((network adj attached adj storage) or NAS) same chassis	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/22 18:28
S5	18	((network adj attached adj storage) or NAS) same chassis same I/O	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/22 17:47
S6	6964	(circuit adj board) same chassis	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/22 18:29
S7	87	((circuit adj board) same chassis) and NAS	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/22 18:29
S8	89	((circuit adj board) same chassis) and (network adj attached adj storage)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/22 18:35

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S9	92	(I/o adj processor) and (network adj attached adj storage)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/22 18:36
S10	56	(I/o adj processor) and (network adj attached adj storage) and chassis	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/22 18:36
S11	2630	(network adj attached adj storage)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/23 09:50
S12	187	(network adj attached adj storage) same I/O	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/23 09:23
S13	4	(network adj attached adj storage) same (I/O adj processor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/23 09:22
S14	0	(network adj attached adj storage) same I/O same chasis	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/23 09:23
S15	12	(network adj attached adj storage) same I/O same chassis	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/23 09:26
S16	2009	I/O same chassis	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/23 09:26

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S17	16	(I/O adj processor) same chassis	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/23 09:27
S18	92	(network adj attached adj storage) and (I/o adj processor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/23 09:42
S19	56	(network adj attached adj storage) and (I/o adj processor) and chassis	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/23 09:42
S20	53	(network adj attached adj storage) same chassis	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/23 09:50
S21	5294	chassis near10 inner	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/03 17:47
S22	142	(chassis near10 inner) with (power)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/03 17:49
S23	2	(chassis near10 inner) with (power) with (I/O adj processor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/03 17:48
S24	2	(chassis near10 inner) same (power) same (I/O adj processor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/03 17:49

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S25	2	(chassis near10 inner) same (I/O adj processor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/03 17:49
S26	16	chassis same (I/O adj processor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/06 13:48
S27	8	chassis same (file adj access)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/06 14:00
S28	12	(chassis or housing) and (file adj access adj processing)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/06 14:03
S29	248	(chassis or housing) and (file adj processing)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/06 14:04
S30	4	(chassis or housing) same (file adj processing)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/06 14:03
S31	3209	(chassis or housing) and (file near5 access$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/06 14:04
S32	170	(chassis or housing) same (file near5 access$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/06 14:04

EAST Search History

S33	2192	(chassis or housing) and (file near5 access$3) and power	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/06 14:05
S34	937	(chassis or housing) and (file near5 access$3) and power and I/O	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/06 14:05
S35	57	(chassis or housing) and (file near5 access$3) and power and (I/O adj processor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/06 14:26
S36	186	(chassis or housing) same (inner adj connector)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/06 14:26
S37	69	(chassis or housing) same (inner adj connector) same (outer adj connector)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/06 14:29
S38	275873	(chassis or housing) same (inner) same (outer)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/06 14:29
S39	75	(chassis or housing) same (inner) same (outer) same NAS	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/06 14:30
S40	2	(chassis or housing) same (inner) same (outer) same (I/O adj processor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/06 14:30

EAST Search History

S41	2	(chassis or housing) same (inner) same (I/O adj processor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/06 14:31
S42	14070	(chassis or housing) same (outer with power)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/06 14:32
S43	8611	(chassis or housing) with (outer with power)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/06 14:34
S44	2	(chassis or housing) with (outer with power) with (I/O adj processor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/06 14:39
S45	8866	(chassis or housing) with ((outer or front) near10 power)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/06 14:40
S46	6088	(chassis or housing) same ((outer or front) near10 power) same (inner or back)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/06 14:42
S47	3	(chassis or housing) same ((outer or front) near10 power) same ((inner or back) near10 I/O)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/06 15:10
S48	712	(361/679).CCLS.	USPAT; USOCR	OR	OFF	2006/07/06 15:05
S49	1008	(361/685).CCLS.	USPAT; USOCR	OR	OFF	2006/07/06 15:05
S50	659	(361/724).CCLS.	USPAT; USOCR	OR	OFF	2006/07/06 15:05
S51	742	(361/748).CCLS.	USPAT; USOCR	OR	OFF	2006/07/06 15:05

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S52	1371	(361/752).CCLS.	USPAT; USOCR	OR	OFF	2006/07/06 15:05
S53	669	(361/807).CCLS.	USPAT; USOCR	OR	OFF	2006/07/06 15:05
S54	7	S46 and S48	USPAT	OR	OFF	2006/07/06 15:06
S55	7	S46 and S49	USPAT	OR	OFF	2006/07/06 15:07
S56	8	S46 and S50	USPAT	OR	OFF	2006/07/06 15:08
S57	0	S46 and S51	USPAT	OR	OFF	2006/07/06 15:06
S58	8	S46 and S52	USPAT	OR	OFF	2006/07/06 15:09
S59	0	S46 and S53	USPAT	OR	OFF	2006/07/06 15:06
S60	19	(chassis or housing) same ((outer or front) near10 power) same ((inner or back or rear) near10 I/O)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/06 15:15
S61	308	(chassis or housing) same ((outer or front) near10 power) same (inner or back or rear) same file	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/06 15:25
S62	4	(chassis or housing) same ((outer or front) near10 power) same (inner or back or rear) same file same I/O	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/06 15:17
S63	14	(chassis or housing) and ((outer or front) near10 power) and (inner or back or rear) same file same I/O	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/06 15:22
S64	1284	(711/114).CCLS.	USPAT; USOCR	OR	OFF	2006/07/06 15:25
S65	1	S61 and S64	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/06 15:39
S66	1189	(711/112).CCLS.	USPAT; USOCR	OR	OFF	2006/07/06 15:38

EAST Search History

S67	0	S61 and S66	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/06 15:39

Interference Searched

	Type	L #	Hits	Search Text	DBs
1	BRS	L5	3	(((inner or back or rear) near10 I/O) with (outer or front) with (chassis or housing)).clm.	US-PGPUB
2	BRS	L6	185	(((inner or back or rear) near10 power) with (outer or front) with (chassis or housing)).clm.	US-PGPUB
3	BRS	L7	280	(((inner or back or rear) near10 power) same (outer or front) same (chassis or housing)).clm.	US-PGPUB
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5	BRS	L9	0	(((inner or back or rear) near10 I/O) with ((outer or front) near10 (power adj connector))).clm.	US-PGPUB
6	BRS	L10	1	(((inner or back or rear) near10 I/O) same ((outer or front) near10 (power adj connector))).clm.	US-PGPUB
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